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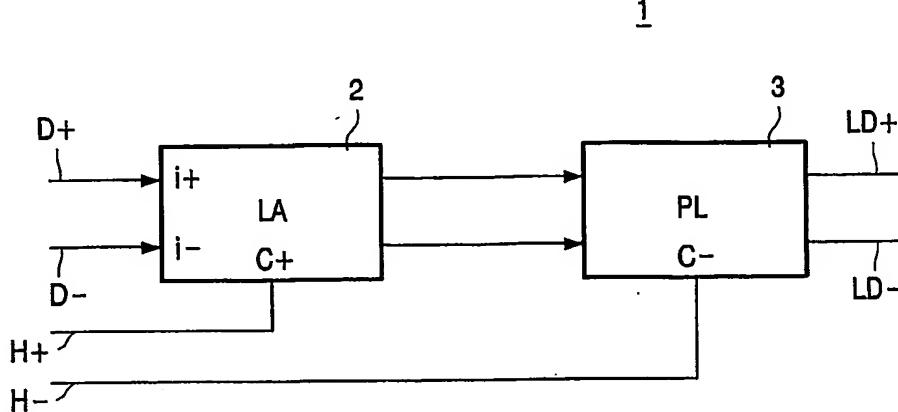
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(54) Title: TRACK AND HOLD CIRCUIT



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(57) Abstract: A track and hold circuit (1) comprising: - a linear amplifier (2) receiving a differential analog signal (D+, D-) and being controlled by a first binary clock signal (H+) having a first phase, - the linear amplifier (2) providing a feed-forward input signal substantially equal with the differential analog signal (D+, D-) to a pseudo latch circuit (3) in the first phase of the first binary clock signal (H+), said pseudo latch circuit (3) being controlled by a second binary clock signal (H-) for memorizing the input signal and providing a differential output signal (LD+, LD-) substantially equal with the input signal during a second phase of the first binary clock signal (H-), the second binary clock signal being substantially in antiphase with the first binary clock signal (H+).



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